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Attorney Docket No.: <u>SONY-11300</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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APR 1 6 2001

Technology Center 2600

In re Application of:

Quan A. Vu et al.

Serial No.: 09/249,642

Filed: February 12, 2000

For: A METHOD OF AND APPARATUS
FOR GENERATING A PRECISE
FRAME RATE IN DIGITAL VIDEO
TRANSMISSION FROM A
COMPUTER SYSTEM TO A
DIGITAL VIDEO DEVICE

TRANSMITTAL LETTER

Group Art Unit: 2612

Examiner: Wilson, J.

260 Sheridan Avenue, Suite 420 Palo Alto, CA 94306

(650) 833-0160

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Enclosed please find a Supplemental Information Disclosure Statement and Form PTO-1449, including copies of the references contained thereon, for filing in the U.S. Patent and Trademark Office.

A \$240.00 check is also enclosed pursuant to 37 C.F.R. § 1.17(p) for filing this Supplemental Information Disclosure Statement after three months as set forth in 37 C.F.R. § 1.97(c).

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. <u>08-1275</u>. An originally executed duplicate of this transmittal is enclosed for this purpose.

Respectfully submitted,

HAVERSTOCK & OWENS LLP

Dated: April 6, 2001

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Jonathan O. Owens Reg. No.: 37,902

Attorneys for Applicants

CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

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HAVERSTOCK & OWENS LLP

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Attorney Docket No.: SONY-1130 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

In re Application of:	Group Art Unit: 2612
Quan A. Vu et al.) Examiner: Wilson, J.
Serial No.: 09/249,642) SUPPLEMENTAL INFORMATION
Filed: February 12, 2000	DISCLOSURE STATEMENT)
For: A METHOD OF AND APPARATUS FOR GENERATING A PRECISE FRAME RATE IN DIGITAL VIDEO TRANSMISSION FROM A COMPUTER SYSTEM TO A DIGITAL VIDEO DEVICE) 260 Sheridan Avenue, Suite 420) Palo Alto, California 94306) (650)833-0160

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicants have become aware of the following printed publications which may be material to the examination of this application:

- U.S. Patent No. 4,713,702;
- U.S. Patent No. 5,128, 677;
- U.S. Patent No. 5,400,340;
- U.S. Patent No. 5,402,419;
- U.S. Patent No. 5,412,698;

04/12/2001 JADD01 00000043 09249642

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Repln. Ref: 04/12/2001 JADD01 DAB:081275 Name/Number:0924 Name/Number:09249642 \$60.00 CR FC: 704

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CERTIFICATE OF MAILING (37 CFR § 1.8(a))

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Attorney Docket No.: SONY-11300

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Technology Center 2600

- U.S. Patent No. 5,434,860;
- U.S. Patent No. 5,444,709;
- U.S. Patent No. 5,493,570;
- U.S. Patent No. 5,546,389;
- U.S. Patent No. 5,594,734;
- U.S. Patent No. 5,596,581;
- U.S. Patent No. 5,598,415;
- U.S. Patent No. 5,602,841;
- U.S. Patent No. 5,621,725;
- U.S. Patent No. 5,668,811;
- U.S. Patent No. 5,710,773;
- U.S. Patent No. 5,781,599;
- European Publication No. 0 428 111 A2;
- Teener, M., "A Bus on a Diet The Serial Bus Alternative: An Introduction to the P1394 High Performance Serial Bus," Apple Computer Inc, Santa Clara, CA, Doc. No. XP000340753, 1992 IEEE;
- "Architecture for High Performance Transparent Bridges," Software Patent Institute Database of Software Technologies, Record 10, Serial number TDB0792.0100, July, 1992;
- "Data Link Driver Program Design for the IBM Token Ring Network PC
 Adapter," Computer Communications, pp. 266-272, No. 5, October 12, 1989,
 GB; and
- Bloks, R.H.J, "The IEEE-1394 High Speed Serial Bus," Philips Journal of Research, pp. 209-216, Vol. 50, No. 1/2, 1996.



Attorney Docket No.: SONY-11306/VED

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27 CEP 88 1.56 and 1.97

This Supplemental Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: April 6,2001

Jonathan O. Owens

Reg. No.: 37,902

Attorneys for Applicants

U.S. Department of Commerce Patent and Trademark Office

Attorney Docket No.: SONY-11300

TION DISCLOSURE STATEMENT BY APPLICANT (Use Several Sheets If Necessary)

Applicant: Quan A. Vu et al.

Serial No.: 09/249,64 CENTO Filing Date: February 12, 2000 U.S. PATENT DOCUMENTS Examiner Serial / Patent Issue Date Filing Date Applicant / Patentee Class Subclass Initials Number AA 4,713,702 12/15/87 Ishihara et al. 360 79 9/5/84 ΑB 7/7/92 9/15/89 5,128, 677 Donovan et al. 341 177 3/4/93 AC 5,400,340 3/21/95 Hillman et al. 370 105.3 5,402,419 3/28/95 AD Osakabe et al. 370 85.1 12/17/93 ΑE 5,412,698 5/2/95 Van Brunt et al. 375 373 3/16/93 ΑF 5,434,860 7/18/95 Riddle 370 84 4/20/94 5,444,709 8/22/95 9/30/93 AG Riddle 370 94.1 AΗ 5,493,570 2/20/96 Hillman et al. 370 105.3 3/15/95 ΑI 5,546,389 8/13/96 Wippenbeck et al. 370 60 7/12/94 ΑJ 5,594,734 1/14/97 Worsley et al. 370 395 11/1/93 5,596,581 ΑK 1/21/97 370 394 6/3/94 Saeijs et al. 5,598,415 1/28/97 8/4/95 AL Nuber et al. 370 474 AM 5,602,841 2/11/97 Lebizay et al. 370 413 3/15/95 AN 5,621,725 4/15/97 Kawamura et al. 370 43 5/24/95 ΑO 5,668,811 9/16/97 Worsley et al. 370 424 6/7/95 7/14/95 ΑP 5,710,773 1/20/98 Shiga 370 512 AQ 5,781,599 7/14/98 Shiga 375 376 9/6/95 FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS Translation Document **Publication Date** Country / Patent Office Class Subclass Number Yes No 5/22/91 G06F AR 0 428 111 A2 ΕP 13/28 AS AΤ OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication) Teener, M., "A Bus on a Diet - The Serial Bus Alternative: An Introduction to the P1394 High Performance Serial Bus," Apple Computer ΑU Inc, Santa Clara, CA, Doc. No. XP000340753, 1992 IEEE. "Architecture for High Performance Transparent Bridges," Software Patent Institute Database of Software Technologies, Record 10, Serial ΑV number TDB0792.0100, July, 1992. "Data Link Driver Program Design for the IBM Token Ring Network PC Adapter," Computer Communications, pp. 266-272, No. 5, October AW 12, 1989, GB. ΑX Bloks, R.H.J, "The IEEE-1394 High Speed Serial Bus," Philips Journal of Research, pp. 209-216, Vol. 50, No. 1/2, 1996. ΑY Examiner: Date Considered: EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.